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FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 023925-00013	SERIAL NO. 09/920,944
LIST OF REFERENCES CITED BY APPLICANT <i>(Use several sheets if necessary)</i>		APPLICANT TZENG		
		FILING DATE August 3, 2001		GROUP To Be Assigned

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LK	AA	5,555,398	Sep. 10, 1996	Raman			Apr. 15, 1994
	AB	5,644,784	Jul. 1, 1997	Peek			Mar. 3, 1995
	AD	5,828,653	Oct. 27, 1998	Goss			Apr. 26, 1996
	AE	5,423,015	Jun. 6, 1995	Chung			May 21, 1991
	AF	5,696,899	Dec. 9, 1997	Kalwitz			Nov. 18, 1992
	AG	6,061,351	May 9, 2000	Erimli et al.			Dec. 18, 1997
	AH	5,748,631	May 5, 1998	Bergantino et al.			May 9, 1996
	AI	6,119,196	Sep. 12, 2000	Muller et al.			Jun. 30, 1997
	AJ	6,175,902 B1	Jan. 16, 2001	Runaldue et al.			Dec. 18, 1997
	AK	5,473,607	Dec. 5, 1995	Hausman et al.			Aug. 9, 1993
	AL	5,787,084	Jul. 28, 1998	Hoang et al.			Jun. 5, 1996
	AM	5,845,081	Dec. 1, 1998	Rangarajan et al.			Sep. 3, 1996
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	AP	6,011,795	Jan. 4, 2000	Varghese et al.			Mar. 20, 1997
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	AX	5,909,686	Jun. 1, 1999	Muller et al.			Jun. 30, 1997
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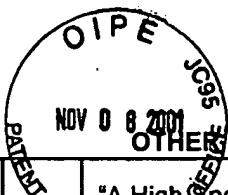
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CK	BE 5,918,074	Jun 29, 1999	Wright et al.			Jul. 25, 1997
	BF 5,898,687	Apr. 27, 1999	Harriman et al.			Jul. 24, 1996
	BG 5,940,596	Aug. 17, 1999	Rajan et al.			Aug. 4, 1997
	BH 5,802,052	Sep. 1, 1998	Venkataraman			Jun. 26, 1996
	BI 5,842,038	Nov. 24, 1998	Williams et al.			Oct. 10, 1996
	BJ 5,742,613	Apr. 21, 1998	MacDonald			Apr. 27, 1993
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	BO WO/9900936	Jan. 7, 1999	WIPO			xx
	BP EP 0 465 090 A1	Jan. 8, 1992	EPO			xx
	BQ EP 0 859 492 A2	Aug. 19, 1998	EPO			xx
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	BT 4-189023	Jul. 7, 1992	Japan			Abs
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	BY EP 0 854 606 A2	Jul. 22, 1998	EPO			xx
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	CB WO 99/00950	Jan. 7, 1999	WIPO			xx
	CC WO 99/00939	Jan. 7, 1999	WIPO			xx
	CD WO 99/00938	Jan. 7, 1999	WIPO			xx
	CE WO 99/00944	Jan. 7, 1999	WIPO			xx
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	CJ	"Local Area Network Switch Frame Lookup Technique for Increased Speed and Flexibility," 700 IBM Technical Disclosure Bulletin 38(1995) July, No. 7, Armonk, NY, US, pages 221-222
	CK	"Queue Management for Shared Buffer and Shared Multi-buffer ATM Switches," Yu-Sheng Lin et al., Department of Electronics Engineering & Institute of Electronics, National Chiao Tung University, Hsinchu, Taiwan, R.O.C., March 24, 1996, pages 688-695
	CL	"A 622-Mb/s 8 x 8 ATM Switch Chip Set with Shared Multibuffer Architecture," Harufusa Kondoh et al., 8107 IEEE Journal of Solid-State Circuits 28(1993) July, No. 7, New York, US, pages 808-814
	CM	"Catalyst 8500 CSR Architecture," White Paper XP-002151999, Cisco Systems Inc. 1998, pages 1-19
LK	CN	"Computer Networks," A.S. Tanenbaum, PRENTICE-HALL INT., USA, XP-002147300(1998), Sec. 5.2-Sec. 5.3, pages 309-320

EXAMINER		DATE CONSIDERED
*EXAMINER:	3/3/05	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 58269.00013	SERIAL NO. 09/920,944
LIST OF REFERENCES CITED BY APPLICANT <i>(Use several sheets if necessary)</i>		APPLICANT Shirjie Tzeng	
		FILING DATE August 3, 2001	GROUP 2661

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EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA						
	AB						
	AC						
	AD						APR 26 2004
	AE						Technology Center 2600
	AF						

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		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION YES NO PART.
LF	AG	WO 00/03256	01/20/2000	PCT			X
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	AL	LEVEL ONE: "Level One (TM) IXP1200 Network Processor" Level One, September 1999, XP002187948; pages 1-51.
	AM	NOUREDDINE W. et al. "Selective Back-Pressure in Switched Ethernet Lans" 1999 IEEE Global Telecommunications Conference; pages 1-9; XP 002258500
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	AO	GOPAL I. et al. "ATM Support in a Transparent Network" Proceedings of the Global Telecommunications Conference, New York, IEEE, US, vol. 1, 6 December 1992; pages 301-307; XP010062747.

EXAMINER

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